



IFW
2826

Attorney Docket SEL 174

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of)
)
Yamazaki et al.)
)
Serial No.: 09/544,801)
)
Filed: April 7, 2000)
)
For: Semiconductor Device And Method)
For Fabricating The Same)
)
Art Unit: 2826)
)
Examiner: Fazli Erdem)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
the Commissioner for Patents ,
P.O. Box 1450, Alexandria, VA 22313-1450 on

January 26 2006
(Date of Deposit)

Shannon Wallace

Name of applicant, assignee, or Registered Rep.

Shannon Wallace 1/26/06
Signature Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE (F) TO OFFICE ACTION

Sir:

Applicants have the following response to the Office Action dated November 4, 2005 in the above-identified application:

Previously, the Examiner had allowed Claims 10-14, 16-23, 25-27 and 80-83, while only rejecting Claims 68-71 and 76-79 provisionally under the judicially created doctrine of obviousness-type double patenting. The double patenting rejection was overcome in the last amendment.

The Examiner, however, now has the following rejection.